

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(bus buses) near4 select\$3 near4 connect\$3 same control\$4 near4 access\$3 same priorit\$5 same arbit\$5	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/02 15:29
L2	0	(bus buses) near4 select\$3 same connect\$3 and control\$4 near4 access\$3 same priorit\$5 same arbit\$5	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/02 15:29
L3	483	(bus buses) near4 select\$3 same connect\$3 and control\$4 near4 access\$3 same priorit\$5	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/02 15:29
S1	113	"integrated circuit" same semiconductor same memory same "I/O" same control\$3 same bus\$2 same ("CPU" "processor")	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/28 16:59
S2	100	"integrated circuit" same semiconductor same memory same "I/O" same control\$3 same bus\$2 same ("CPU" "processor") NOT TCP	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/28 17:06
S3	61	"integrated circuit" same semiconductor same memory same "I/O" same control\$3 same bus\$2 same ("CPU" "processor") same access\$3 NOT TCP	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/28 18:34
S5	15	semiconductor and "integrated circuit" same memory same "I/O" same controller\$1 same (bus buses) same ("CPU" "processor") same access\$3 same outside NOT TCP	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/28 18:53
S6	25	semiconductor and "integrated circuit" same memory same "I/O" same peripheral\$1 same controller\$1 same (bus buses) same ("CPU" "processor") same access\$3 and outside NOT TCP	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/28 19:11
S7	0	semiconductor and "integrated circuit" same memory same "I/O" same peripheral\$1 same controller\$1 same (bus buses) same ("CPU" "processor") same access\$3 and outside same request\$3 same access\$3 NOT TCP	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/28 19:11

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S8	1	semiconductor and "integrated circuit" same memory same "I/O" same peripheral\$1 same controller\$1 same (bus buses) same ("CPU" "processor") same access\$3 and outside same request\$3 NOT TCP	US-PGPUB; USPAT; USOCR	OR	OFF	2006/04/28 19:12
S9	4	semiconductor and "integrated circuit" same memory same ("I/O" "input/output" (input adj output)) same peripheral\$1 same controller\$1 same (bus buses) same ("CPU" "processor") same access\$3 and outside same request\$3 NOT TCP	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 14:56
S10	194	endo.in. and semiconductor.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 11:06
S11	23	endo.in. and semiconductor.ti. and integrated.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 11:06
S12	139	access adj port\$1 same (bus buses) same RAM	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 15:03
S13	44	access adj port\$1 same (bus buses) same RAM same controll\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 15:03
S14	49	access adj port\$1 same (bus buses) same RAM same control\$4 same (CPU\$1 processor\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 15:05
S16	357	port\$1 same (bus buses) same RAM same access\$2 near2 control\$4 same (CPU\$1 processor\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 15:06
S17	12	port\$1 same (bus buses) same RAM same access\$2 near2 control\$4 same (CPU\$1 processor\$1) and port\$1.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 15:07
S18	17	port\$1 same (bus buses) same RAM same access\$2 near2 control\$4 same (CPU\$1 processor\$1) and (multiport\$1 port\$1).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 15:17
S19	1204	"dual-port" adj RAM	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 15:17
S20	84	"dual-port" adj RAM same (bus buses) same port\$1 same control\$4 same access\$2	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 16:05

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S21	12	"dual-port" adj RAM same (bus buses) same port\$1 same control\$4 same access\$2 same select\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 16:38
S22	495	(bus buses) near3 select\$3 same port\$1 same control\$4 same access\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 16:39
S23	40	(bus buses) near3 select\$3 same port\$1 same control\$4 near3 access\$3 same memory	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 16:45
S24	1	(bus buses) near3 select\$3 same port\$1 same control\$4 near3 access\$3 same memory same priorit\$5	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 16:46
S25	3	(bus buses) near3 select\$3 same port\$1 same control\$4 near4 access\$3 same priorit\$5	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 16:46
S26	75	(bus buses) near3 select\$3 same control\$4 near4 access\$3 same priorit\$5	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 16:48
S27	92	(bus buses) near4 select\$3 same control\$4 near4 access\$3 same priorit\$5	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/01 16:52
S28	0	(bus buses) near4 select\$3 same control\$4 near4 access\$3 same priorit\$5 same aribit\$5	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/02 15:28